

**AMENDMENTS TO THE SPECIFICATION**

Please amend the first full paragraph on page 11 as follows:

Fig [[2]]3 shows the system having an input end at which the source bytestream is received and an output end where the final output frame 24 is output from the system. However, it will be understood that the system may include intermediate inputs and outputs at other intermediate stages, such as for fetching data content or for saving/converting data generated in the course of the process.